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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Satwant Singh et al.

Title:

DELAY-MATCHED ASIC CONVERSION OF A PROGRAMMABLE

LOGIC DEVICE

Application No.:

10/660,814

Filing Date:

September 12, 2003

Examiner:

Daniel D. Chang

Group Art Unit:

2819

Docket No.:

M-15198 US

Confirmation No.

7052

Irvine, California May 16, 2005

Via Facsimile to (703) 872-9306

Mail Stop AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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- Amendment Transmittal Letter
- 2) Response to Office Action w/attached Replacement Sheet

Dated: May 16, 2005

Saundra L Carr

Number of pages (including this sheet):

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Dear Sir:

Mul	tiple Dependent Claims	\$360	(if applicable)						\$.0			
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Total Claims		23	•	37	=	0	×	\$50.00	\$.0			
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XX	Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 50-2257						

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date shown below.

Saundra Carr

May 16, 2005

Date of Signature

Respectfully submitted.

Altorney for Applicants Reg. No. 42,622

Tel.: (949) 752-7040

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE CEIVED

Applicant(s):

Satwant Singh; Cyrus Tsui

CENTRAL FAX CENTER

Assignee:

Lattice Semiconductor Corporation

MAY 1 6 2005

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Delay-Matched ASIC Conversion of a Programmable Logic Devices

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RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action dated February 14, 2005, Applicants submit the following amendments and remarks.

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Serial No.

10/660,814